



Si2 Board of Directors Nomination Form

Response required by May 15, 2020

Incomplete forms will not be considered for balloting.

Current board members need not be renominated.

1. Nominated Company _____

Note: Company representatives who are not currently serving as officers of their company require written approval by an officer of their company.

Representative Name	
Title	
Address	
City, State, Zip	
Country	
Phone	
Fax	
E-mail	

2. Please provide a statement from the nominee and attach it to this form for submission. An example Nomination Statement is provided on the last page in this packet. Keep in mind that it is the company elected rather than the representative. If the representative is unable to complete his/her term of service, the elected company shall appoint a new representative, in accordance with the guidelines in the Information Form

3. Company representative agrees to devote a minimum of one day per month to Si2 Board activity?

Yes

No (if no, please specify why)

4. Nominated by (leave blank if self-nomination)

Name	
Company	
Phone	
Fax	
E-mail	
Date	
Signature	

5. Company Category

- EDA Software Customer (User)
- EDA Software Provider (Supplier)

Nominee Signature _____

Date _____

Nominator Signature _____

Date _____

Officer Approval (if applicable) _____

Date _____

6. Nomination Form Submission

Current board members need not be renominated.

For consideration, please email your completed nomination form and a Nomination Statement based on the following sample. Electronic versions are acceptable. A photo of the nominee should also be sent to Terry Berke, tberke@si2.org

Please don't hesitate to call with any questions at (512) 917-1358

Sample Nomination Statement

(Note, if you send a photo, it will be included with the Ballot)

Company Name: IBM
Representative Name: Leon Stok
Representative Title: Vice President

Company Description: Please provide up to 200 words that describe the company and how it can assist in advancing the Si2 mission. A sample is provided below:

About IBM Corporation: The systems and products designed by IBM rely heavily on the ability to quickly and accurately design complex silicon functions. Over the last decades the silicon content of IBM systems has increased drastically. This requires a significant improvement in designer productivity. Something that can only be realized by design tools that are operating in an as seamless flows as possible. Our design flows consist of a mixture of internally developed and externally acquired tools from most EDA vendors. The interoperability of this entire toolset is essential to deliver the productivity required.

Therefore, IBM is a strong supporter of open EDA standards and technologies and vigorously supports the objectives of Si2.

Representative's Biography: Please provide 200-300 words summarizing the industry experience of the nominated company's representative, and why he/she would be a valuable member of the Si2 Board of Directors. A sample is provided below:

Leon Stok is the vice president of IBM's Electronic Design Automation group. Prior to this he held positions as Director of EDA, Executive Assistant to IBM's Senior Vice President of Technology and Intellectual Property and executive assistant to IBM's Senior Vice President of the Technology group.

Leon Stok studied electrical engineering at Eindhoven University of Technology, the Netherlands, from which he graduated with honors in 1986. He obtained a Ph.D. degree from Eindhoven University in 1991. Leon Stok worked at IBM's Thomas J. Watson Research Center as part of the team that developed BooleDozer, the IBM logic synthesis tool. Subsequently he managed IBM's logic synthesis group, and lead all of IBM's design automation research as the Senior Manager Design Automation at IBM Research from 1999-2004.

Dr. Stok has published over fifty papers on many aspects of high level, architectural and logic synthesis, low power design, placement driven synthesis and on the automatic placement and routing for schematic diagrams. He was elected an IEEE fellow for the development and application of high-level and logic synthesis algorithms.