MEMBER AND GUEST COMPANIES ONLY

8:00 AM  Breakfast (30 min.)
8:30 AM  Agenda Review (10 min.)  Peter Lee (Micron)
8:40 AM  Treasurer's Report (5 min.)  Takeshi Naito (Toshiba)
8:45 AM  Update from Si2 (30 min.)  John Ellis (Si2)
9:15 AM  GaN HEMT Model Standardization Status (60 min.)  Samuel Mertens (Cadence)
10:15 AM Break (15 min.)
10:30 AM BSIM4 Versioning (60 min.)  Introduction by Peter Lee (Micron)
ALL (includes Model Developers)
11:30 AM  EDA Vendor Reports (30 min.)  Vendors will rotate into the room
12:00 PM Group Photo
12:10 PM Lunch (50 min.)
Text in green: Not yet confirmed.

**Thursday, December 8, 2016**

WiFi: Network: xxxx; Password: yyy

**ALL (includes Model Developers)**

1:00 PM  Web Site Updates (15 min.)  Richard Williams (IBM)
1:15 PM  HICUM (75 min.)  Didier Celi (ST Micro)
2:30 PM  Mextram (45 min.)  Wei Chen (TI)
3:15 PM  Break (15 min.)
3:30 PM  BSIM-CMG (20 min)  Richard Williams (IBM)
3:50 PM  BSIM-Bulk (BSIM4, BSIM6) (30 min.)  Kaiman Chan (TI)
4:20 PM  BSIM-SOI (10 min)  Shrinivas Pandharpure (GLOBALFOUNDRIES)
4:30 PM  Model QA and Release, and BSIM Model Versioning (30 min.)  Shahriar Moinian (Broadcom)
5:00 PM  New generation reliability model (40 min.)  Jushan Xie (Cadence) and Ru Huang (Peking University)
5:40 PM  Adjourn and Reception: Cafeteria and Outside (120 min)
Agenda for the 2016Q4 CMC Meeting  
Cadence Design Systems, San Jose, CA  
Auditorium, Building 10  
Friday, December 9, 2016  
WiFi: Network: xxxx; Password: yyyy

Text in green: Not yet confirmed.

**ALL (includes Model Developers)**

<table>
<thead>
<tr>
<th>Time</th>
<th>Event</th>
<th>Presenter</th>
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<tbody>
<tr>
<td>8:00 AM</td>
<td>Breakfast (30 min.)</td>
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<tr>
<td>8:30 AM</td>
<td>Agenda Review (10 min.)</td>
<td>Peter Lee (Micron)</td>
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<tr>
<td>8:40 AM</td>
<td>Next Meeting Locations (10 min.)</td>
<td>Joe Watts (GLOBALFOUNDRIES)</td>
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<tr>
<td>8:50 AM</td>
<td>BSIM-IMG (15 min)</td>
<td>Joe Watts (GLOBALFOUNDRIES)</td>
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<td>9:05 AM</td>
<td>HiSIM2 (30 min.)</td>
<td>Shigetaka Kumashiro (Renesas)</td>
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<tr>
<td>9:35 AM</td>
<td>HiSIM_HV (30 min.)</td>
<td>Takeshi Naito (Toshiba) for Ehrenfried Seebacher (ams)</td>
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<tr>
<td>10:05 AM</td>
<td>Break (15 min.)</td>
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**Agenda for the 2016Q4 CMC Meeting**  
Cadence Design Systems, San Jose, CA  
Auditorium, Building 10  

**Friday, December 9, 2016**  
WiFi: Network: xxxx;  Password: yyy

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<tr>
<th>Time</th>
<th>Session</th>
<th>Speaker(s)</th>
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<tbody>
<tr>
<td>10:20 AM</td>
<td>HiSIM_SOI (5 min.)</td>
<td>Marek Mierzwinski (Keysight)</td>
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<tr>
<td>10:25 AM</td>
<td>HiSIM_SOTB (5 min.)</td>
<td>Marek Mierzwinski (Keysight)</td>
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<td>10:30 AM</td>
<td>PSP (20 min)</td>
<td>Andries Scholten (NXP)</td>
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<td>10:50 AM</td>
<td>OMI (20 min.)</td>
<td>Colin Shaw (Silvaco)</td>
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<td>11:10 AM</td>
<td>ESD (20 min.)</td>
<td>Joe Watts for You Li</td>
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<td>(GLOBALFOUNDRIES)</td>
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<td>11:30 AM</td>
<td>Reliability Standardization (20 min.)</td>
<td>Introduction by Peter Lee (Micron)</td>
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<td>11:50 PM</td>
<td>Lunch (60 min.)</td>
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ALL (includes Model Developers)

12:50 PM  Collaboration with NEEDS: Model QA 3.0 Update (15 min.)  Richard Williams (IBM) for Xufeng Wang (Purdue)
1:05 PM  Verilog-A Updates (15 min.)  Geoffrey Coram (ADI)
1:20 PM  One-page Group Summaries (30 min.)  Working group chairs
1:50 PM  New Projects (30 min.)  Peter Lee (Micron)
2:20 PM  CMC Survey (30 min.)  Peter Lee (Micron)
2:50 PM  Next Meeting Planning (10 min.)  All
3:00 PM  Action Item Review (10 min.)  Richard Williams (IBM)
3:10 PM  Adjourn